<u>IN THE UNITED STATES PATENT AND TRADEMARK OF</u>FICE

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Applicant:	Frank O'Mahon	y et al. BOX	ΔΕ				
Title:	LOW LOSS	INTERCOENECT ST	RUCTURE F	OR USE	IN	MICROEL	ECTRONIC.
	CIRCUITS	\(\frac{1}{2}\)				1.6	2
Docket No :	884 405US1	CED U 2 2003 H	Serial No.:	09/893023	••		

MS AF

Filed:

Examiner:

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Customer No.: 21186

June 27, 2001

Ahmed Sefer

We are transmitting herewith the following attached items (as indicated with an "X"):

A return postcard.

An Amendment and Response Under 37 CFR 1.116 (10 Pages).

Communication Concerning Related Patents (1 pg.).

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

Name

EF 2200

Due Date: October 16, 2003

Group Art Unit: 2826

Confirmation No.: 3406

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this \mathcal{L} day of August, 2003.

Kacia Zii KACIA LEE

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

(GENERAL)

S/N 09/893023

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Frank O'Mahony et al.

Examiner: Ahmed Sefer

Serial No.:

09/893023

Group Art Unit: 2826

Filed:

June 27, 2001

Docket: 884.405US1

Title:

LOW LOSS INTERCONNECT STRUCTURE FOR USE IN

MICROELECTRONIC CIRCUITS

Assignee:

Intel Corporation

Customer No.: 21186

COMMUNICATION CONCERNING RELATED PATENTS

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related patents in the above-identified patent application:

Patent No. 6,522,186

Serial No. 09/893,067

Filing Date

Attorney Docket

June 27, 2001

884.431US1

Respectfully submitted,

FRANK O'MAHONY ET AL.

By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

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Date 24 Hugust 2003

David R. Cochrar

Reg. No. 46,632

CFRTIFICATE UNDER 37 CFR 1.8 The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop AF, Commissioner of Patents, P.O. Box 1450,

Alexandria, VA 22313-1450, on this $\frac{29}{4}$ day of August, 2003.

KACIA LEE

Signature Lilia Lil

Name

EXPEDITED PROCEDURE - EXAMINING GROUP 2826

S/N 09/893023 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Frank O'Mahony et al.

Examiner: Ahmed Sefer

Serial No.:

09/893023

Group Art Unit:\2826

Filed:

June 27, 2001

Docket No.: 884.405US1

OIP & Title:

LOW LOSS INTERCONNECT STRUCTURE FOR USE IN

MICROELECTRONIC CIRCUITS

SEP 0 2 2003 ssignee:

Intel Corporation

Customer No.: 21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In response to the final Office Action mailed July 16, 2003, please amend the application as follows: